

ABSTRACT OF THE DISCLOSURE

A display device of the present invention has an incorporated charge pump control circuit and a clock frequency of a switching element can be varied according to a display mode to reduce the power consumption. On the display device, a variable frequency-dividing circuit and a CPU to control the circuit are configured with thin film transistors. A clock frequency of a switching element is varied by controlling a dividing ratio of the variable frequency-dividing circuit on the basis of CPU data.

5